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; Author : ADI - Apps www.analog.com/MicroConverter

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; Date : October 2003

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; File : ADCcont.asm

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; Hardware : ADuC841

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; Description : Performs ADC conversions in continuous mode at a

; rate of 138KSPS (assuming an 11.0592MHz Mclk).

; Outputs ADC results on P0 & P2. Continuously

; flashes LED (independently of ADC routine) at

; approximately 5Hz.

; All rate calculations assume an 11.0592MHz Mclk.

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$MOD841 ; Use 8052&ADuC841 predefined symbols

LED EQU P3.4 ; P3.4 drives red LED on eval board

CHAN EQU 0 ; convert this ADC input channel..

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; BEGINNING OF CODE

CSEG

ORG 0000h

JMP MAIN ; jump to main program

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; INTERRUPT VECTOR SPACE

ORG 0033H ; (ADC ISR)

MOV P0,ADCDATAL ; ADC result low byte to Port0

MOV P2,ADCDATAH ; high nibble and channel ID to Port2

RETI

;====================================================================

; MAIN PROGRAM

ORG 004Bh

MAIN:

; PRECONFIGURE...

MOV ADCCON1,#09Ch ; power up ADC, ADC Clock = Mclk/4 7.2us conv+acq time

MOV ADCCON2,#CHAN ; select channel to convert

; LAUNCH CONTINUOUS CONVERSIONS...

SETB EA ; enable interrupts

SETB EADC ; enable ADC interrupt

SETB CCONV ; begin continuous conversions

; CONTINUE WITH OTHER CODE...

MOV A,#010 ; set delay length

AGAIN: CPL LED ; blink (complement) the LED

CALL DELAY ; delay 100ms

JMP AGAIN ; repeat

; the micro is free to continue with other tasks (flashing the LED in

; this case) while the ADC is continuously converting, and results

; are being handled by the ADC interrupt service routine.

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; SUBROUTINE

DELAY: ; delay 10ms X A

MOV R5,A ; 1 clock

DLY2: MOV R7,#090h ; 2

DLY1: MOV R6,#0FFh ; 2

DJNZ R6,$ ; 3

DJNZ R7,DLY1 ; 3

DJNZ R5,DLY2 ; 3

RET

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END